

Electronic Acknowledgement Receipt

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Confirmation Number:	3423
Title of Invention:	System and method for suppression of wafer temperature drift in cold-wall cvd systems
First Named Inventor/Applicant Name:	Wiebe De Boer
Customer Number:	65913
Filer:	Peter Zawilski
Filer Authorized By:	
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1		US020527US_RestrictionRe spTraverse_2008-AUG-05.p df	117695 e0285799e0d36a0d729f169f8b2b48a29 0674764	yes	3
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Response to Election / Restriction Filed			1	1	
Applicant Arguments/Remarks Made in an Amendment			2	3	
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2	Information Disclosure Statement (IDS) Filed (SB/08)	US_020527US_IDS_Form SB_08a_2008-AUG-05.pdf	573431 de898ac480f1d3d2156f54f6f5c012649 b31 c643	no	4
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3	NPL Documents	US020527WO1_Invitationto PayFees_2004-JUN-14.pdf	214554 d3070c3ae0a311f4bc805931f048fbc70 272a827	no	4
Warnings:					
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4	Foreign Reference	WO2002065521A1.pdf	1778894 749877c99c2b4b47a56b16742768c41 eb412a67	no	44
Warnings:					
Information:					
5	Foreign Reference	EP1367637A1.pdf	392840 a1fcc3676da2467baa74ad2f93c57eaf af4cee2	no	29
Warnings:					
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6	Fee Worksheet (PTO-06)	fee-info.pdf	8219 14379c3a41824d26e89496cd8c5d97 68bee053	no	2
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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